

# Abstracts

## Optimization of high Q CMOS-compatible microwave inductors using silicon CMOS technology (1997 Vol. I [MWSYM])

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*Min Park, Seonghearn Lee, Hyun Kyu Yu and Kee Soo Nam. "Optimization of high Q CMOS-compatible microwave inductors using silicon CMOS technology (1997 Vol. I [MWSYM])." 1997 MTT-S International Microwave Symposium Digest 1. (1997 Vol. I [MWSYM]): 129-132.*

We present the extensive experimental results showing the important dependences of layout parameters on RF performance of rectangular spiral inductors, in order to determine the optimized layout parameters for designing the high Q inductors used in RF ICs at 2 GHz. The detailed comparative analysis is also carried out to investigate substrate effects by varying the substrate resistivity.

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